

Fig 1

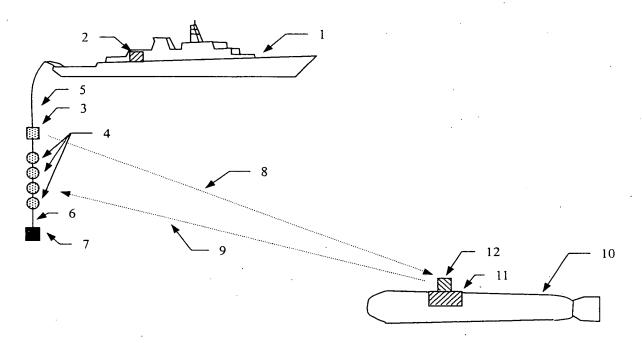


Fig 2

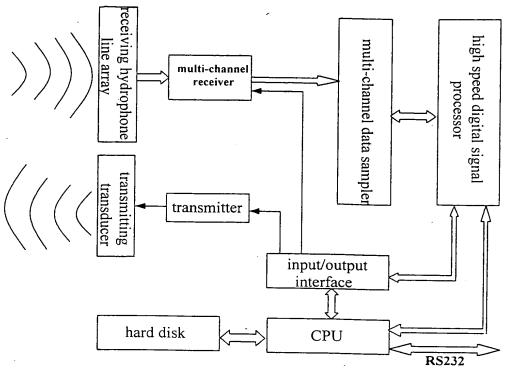
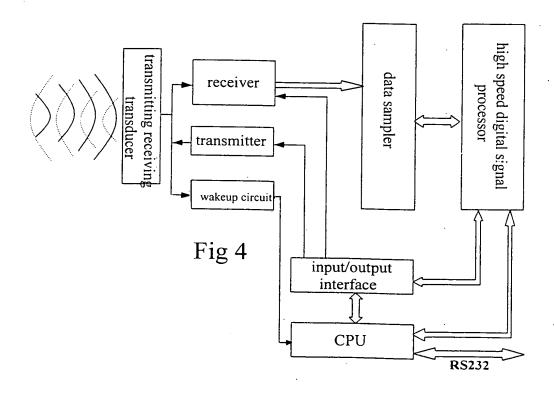
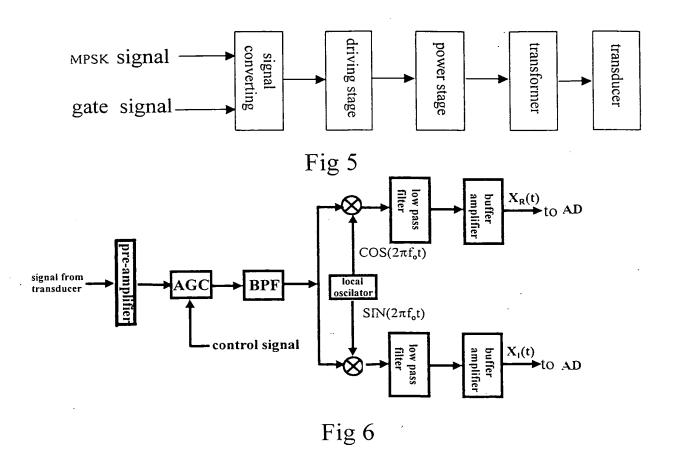
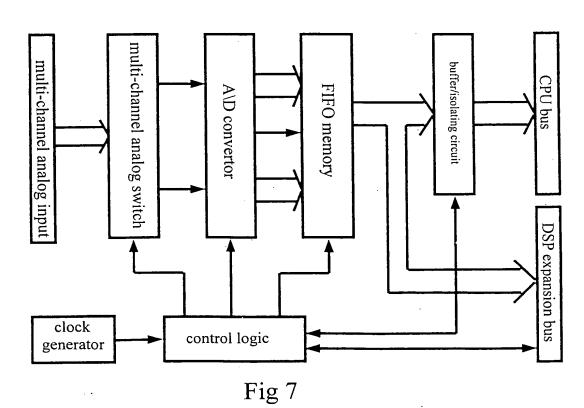
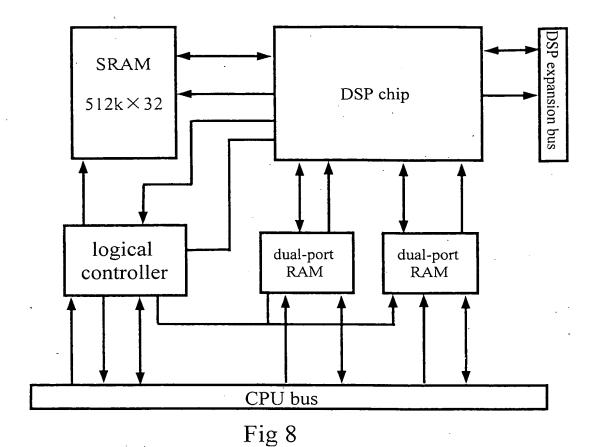


Fig 3









DA DA OC gate digital output port

digital

input port

Fig 9

8 bits

digital

input

logical

controller

main control computer bus

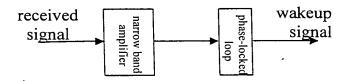


Fig 10

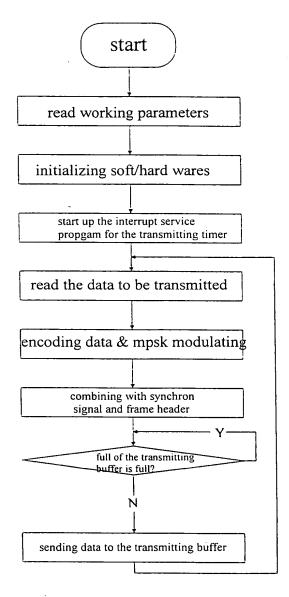


Fig 11a

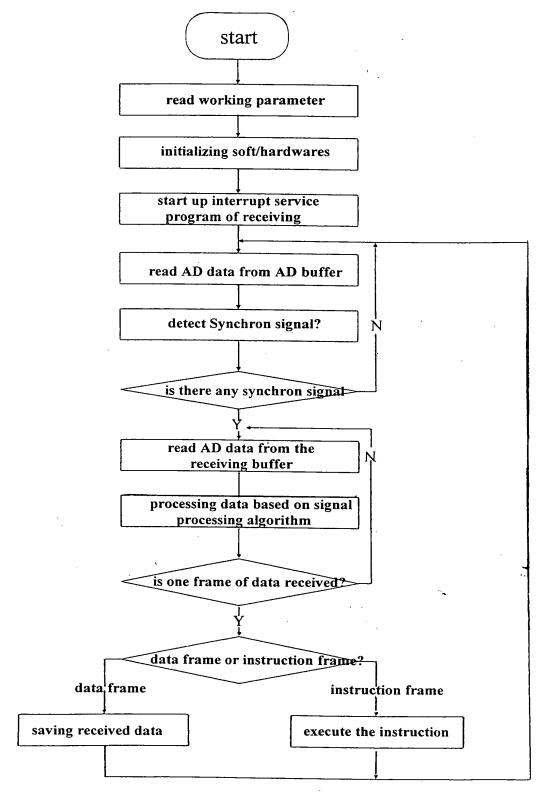


Fig 11b

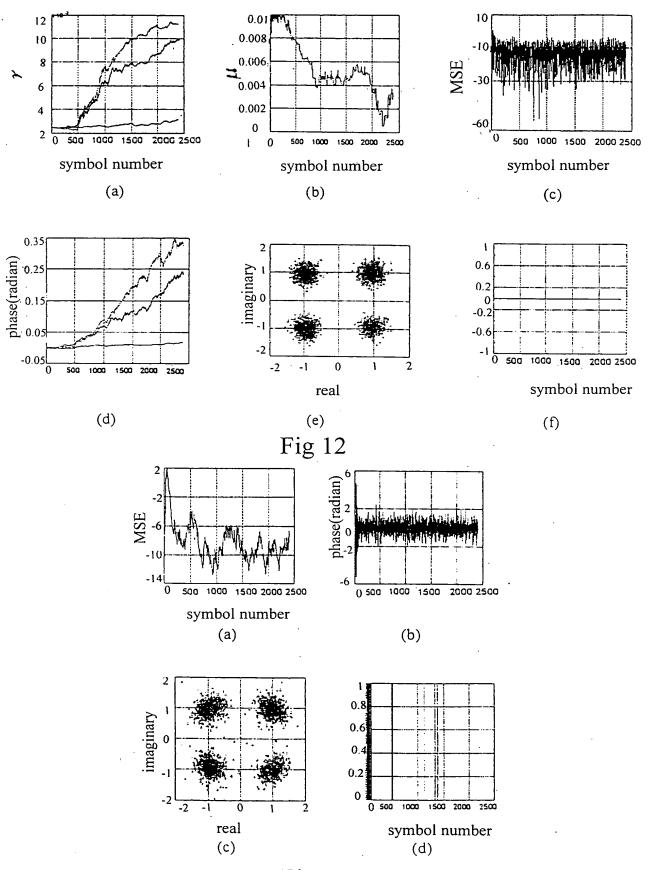


Fig 13

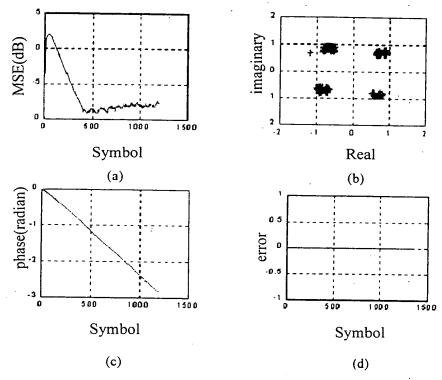


Fig 14



Range 1100m, Range 2000m, Range 4000m, bit error probability 4.2×10⁻⁴ bit error probability 4.22×10⁻⁵ bit error probability 2.6×10⁻⁵

Fig 15

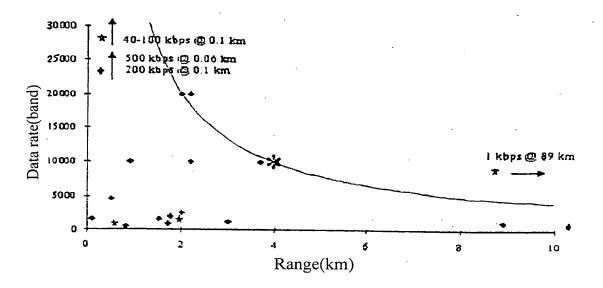


Fig 16

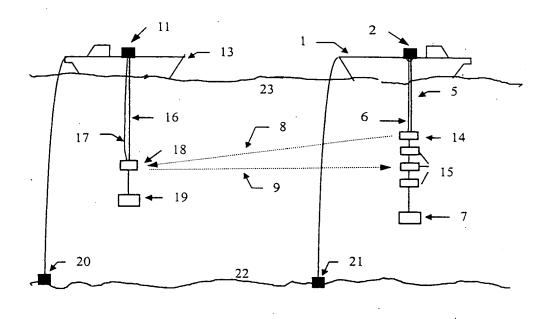


Fig 17

